

IN THE UNITED STATES PATENT OFFICE

Applicants: Warren E. Cory et al.

Assignee: Xilinx, Inc.

Title: Variable Latency Buffer and Method of Operation

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Examiner: Tanh Q. Nguyen Art Unit: 2182

Docket No.: X-1214 US Conf. No.: 4907

Mail Stop Amendment
COMMISSIONER FOR PATENTS
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INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, Applicants bring to the attention of the Examiner the references listed in the attached PTO/SB/08A form.

Citation of the listed references shall not be construed as an admission that the references are necessarily prior art with respect to the instant invention. Citation of the listed references shall not be construed as a representation that a search has been made other than as described above. Also, citation of the listed references shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Based on the Office Action mailed March 25, 2008, no fee is believed due for entry of these references; however, the Commissioner is hereby authorized to charge any fees, or credit any overpayment which may be required to Deposit Account No. 24-0040.

Respectfully submitted,

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*I hereby certify that this correspondence is being filed via EFS-Web with
the United States Patent & Trademark Office on June 12, 2008.*

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